

REMARKS

Claims 1, 3 to 10 and 12 to 18 are pending in this application of which claims 1, 8, 10 and 17 are the independent claims. Favorable reconsideration and further examination are respectfully requested.

Applicant acknowledges the Examiner's indication that claims 1, 3 to 7, 10 and 12 to 18 are allowed. Applicant has amended claims 1, 8 to 10 and 17 to 18.

Claims 8 and 9 were rejected under 35 U.S.C. §112, second paragraph. In particular, the terms "the loaded output" and "the control system" allegedly have insufficient antecedent basis. Accordingly, Applicant has amended claim 8 to include sufficient antecedent basis. In addition, the Examiner also alleges that the limitation of "means for compensating an error signal, the means for compensating coupled between the input and output of the power supply" is unclear. Applicants have amended claim 8 to remove the limitation "the input and output of the power supply." Applicant respectfully requests withdrawal of the §112 rejections.

Claims 8 and 9 were rejected under 35 U.S.C. §102 as being anticipated by U.S. Patent No. 5,504,452 (Takenaka).

Claim 8 as amended is directed to a control loop circuit. The control loop circuit includes a main loop amplifier having an input and an output and an output stage amplifier having an input and an output. The output of the main loop amplifier connected to the input of the output stage amplifier, the output of the output stage amplifier is connected to a variable load. The control loop circuit also includes a compensation means for compensating an error signal and a dynamic compensation control loop including a digital-signal-processor. The dynamic

compensation control loop connects the output of the main loop amplifier to the input of the main loop amplifier. The compensation means connects the output of the output stage amplifier to the dynamic compensation loop. The dynamic compensation control loop selectively cooperates with the compensation means to optimize the output of the output stage amplifier in response to the variable load.

The applied art is not understood to disclose or to suggest the foregoing features of claim 10. In particular, Takenaka does not disclose or suggest that a compensation means connects the output of a output stage amplifier to a dynamic compensation loop.

The Examiner indicated on page 3 of the office action as the basis for his rejection that transistor 12 is a main stage amplifier. Even if transistor 12 is a main stage amplifier, transistor 12 is connected directly to an internal circuit 14 without any other intervening devices; e.g., an output stage amplifier. Therefore, Takenaka does not disclose or suggest the output stage amplifier much less that the compensation means connects the output stage amplifier to the dynamic compensation loop. For at least this reason, claim 8 is believed to be allowable.

Applicant submits that all dependent claims now depend directly or indirectly on allowable independent claims.

It is believed that all of the pending claims have been addressed. However, the absence of a reply to a specific rejection, issue or comment does not signify agreement with or concession of that rejection, issue or comment. In addition, because the arguments made above may not be exhaustive, there may be reasons for patentability of any or all pending claims (or other claims) that have not been expressed. Finally, nothing in this paper should be construed as

Applicant : Gerald H. Johnson
Serial No. : 10/000,440
Filed : October 30, 2001
Page : 11 of 11

Attorney's Docket No.: 1495-US

intent to concede any issue with regard to any claim, except as specifically stated in this paper, and the amendment of any claim does not necessarily signify concession of unpatentability of the claim prior to its amendment.


Applicant submits that the entire application is now in condition for allowance. Such action is respectfully requested at the Examiner's earliest convenience.

All correspondence should be directed to the address below. Applicant's attorney can be reached by telephone at (617) 422-3532.

No fee is believed to be due for this Response; however, if any fees are due, please apply such fees to Deposit Account No. 20-0515 referencing Attorney Docket 1495-US.

Respectfully submitted,

Date: 19 October 2004


Anthony T. Moosey
Reg. No. 55,773

Teradyne, Inc.
321 Harrison Avenue, MS-H61
Boston, MA 02118
Telephone: (617) 422-3532
Facsimile: (617) 422-2290